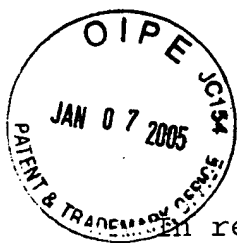


DFW



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re Application of: Zia Hossain et al.
Serial No.: 10/729,292
Filing Date: December 8, 2003
Group Art Unit: 2815
Examiner: Joseph H. Nguyen
Title: **LATERAL FET STRUCTURE WITH IMPROVED
BLOCKING VOLTAGE AND ON RESISTANCE
PERFORMANCE AND METHOD**

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED
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Date of Deposit

Semiconductor Components Industries, LLC.

Name of Assignee

Lydia McNamee
SIGNATURE

1/5/2005
DATE

AMENDMENT

HONORABLE COMMISSIONER FOR PATENTS,
P.O. Box 1450
Alexandria, VA 22313-1450
Sir:

In response to the Office Action mailed October 12, 2004,
please reconsider the above-identified patent application in view
of the amendments and remarks presented hereinafter.